# Pro**Labs**

## 100G-LR4-QSFP2KM-C

Extreme Networks<sup>®</sup> 100G-LR4-QSFP2KM Compatible TAA 100GBase-LR4 QSFP28 Transceiver (SMF, 1295nm to 1309nm, 2km, LC, DOM)

## Features:

- SFF-8665 Compliance
- Duplex LC Connector
- Single-mode Fiber
- Commercial Temperature 0 to 70 Celsius
- Hot Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS Compliant and Lead Free



#### **Applications:**

- 100GBase Ethernet
- Access and Enterprise

#### **Product Description**

This Extreme Networks<sup>®</sup> 100G-LR4-QSFP2KM compatible QSFP28 transceiver provides 100GBase-LR4 throughput up to 2km over single-mode fiber (SMF) using a wavelength of 1295nm to 1309nm via an LC connector. It is guaranteed to be 100% compatible with the equivalent Extreme Networks<sup>®</sup> transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

ProLabs' transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



Rev. 112224

## **Regulatory Compliance**

- ESD to the Electrical PINs: compatible with MIL-STD-883E Method 3015.4
- ESD to the LC Receptacle: compatible with IEC 61000-4-3
- EMI/EMC compatible with FCC Part 15 Subpart B Rules, EN55022:2010
- Laser Eye Safety compatible with FDA 21CFR, EN60950-1& EN (IEC) 60825-1,2
- RoHS compliant with EU RoHS 2.0 directive 2015/863/EU

## **Absolute Maximum Ratings**

| Parameter                  | Symbol | Min. | Тур.     | Max. | Unit |
|----------------------------|--------|------|----------|------|------|
| Maximum Supply Voltage     | Vcc    | -0.5 |          | 4.0  | V    |
| Storage Temperature        | TS     | -40  |          | 85   | °C   |
| Operating Case Temperature | Тс     | -5   |          | +70  | °C   |
| Operating Humidity         | RH     | 5    |          | 95   | %    |
| Data Rate Per Lane         |        |      | 25.78125 |      | Gbps |

## **Electrical Characteristics**

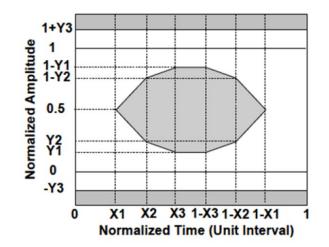
| Parameter                                  | Symbol  | Min. | Тур. | Max. | Unit | Notes |
|--|---------|------|------|------|------|-------|
| Module Supply Current                      | lcc     |      |      | 1100 | mA   |       |
| Power Dissipation                          | PD      |      |      | 3500 | mW   |       |
| Transmitter                                |         |      |      |      |      |       |
| Single-ended Input Voltage Tolerance       |         | -0.3 |      | 4.0  | V    |       |
| AC Common Mode Input Voltage Tolerance     |         | 15   |      |      | mV   |       |
| Differential Input Voltage Swing Threshold |         | 50   |      |      | mVpp |       |
| Differential Input Voltage Swing           | Vin,pp  | 190  |      | 700  | mVpp |       |
| Differential Input Impedance               | Zin     |      | 100  |      | Ω    |       |
| Receiver                                   | 1       |      |      |      |      |       |
| Single-ended Output Voltage                |         | -0.3 |      | 4.0  | V    |       |
| AC Common Mode Output Voltage              |         |      |      | 7.5  | mV   |       |
| Differential Output Voltage Swing          | Vout,pp | 300  |      | 850  | mVpp |       |
| Differential Output Impedance              | Zo      | 90   | 100  | 110  | Ω    |       |

## **Optical Characteristics**

| Parameter                       | Symbol                           | Min.    | Тур.        | Max.    | Unit | Notes |
|---------------------------------|----------------------------------|---------|-------------|---------|------|-------|
|                                 | LO                               | 1294.53 | 1295.56     | 1296.59 |      |       |
| Long Manales ath                | L1                               | 1299.02 | 1300.05     | 1301.09 |      |       |
| Lane Wavelength                 | L2                               | 1303.54 | 1304.58     | 1305.63 | nm   |       |
|                                 | L3                               | 1308.09 | 1309.14     | 1310.19 |      |       |
| Transmitter                     |                                  |         |             |         |      |       |
| Launch Optical Power per lane   | Ро                               | -4.3    |             | +4.5    | dBm  | 1     |
| Total Launch Optical Power      | Ро                               |         |             | +10.5   | dBm  | 1     |
| Extinction Ratio                | EX                               | 4.0     |             |         | dB   | 2     |
| Spectral width(-20dB)           | Δλ                               |         |             | 1       | nm   |       |
| Side Mode Suppression Ratio     | SMSR                             | 30      |             |         | dB   |       |
| Optical Return Loss Tolerance   | ORLT                             |         |             | 20      | dB   |       |
| Pout @TX-Disable Asserted       | Poff                             |         |             | -30     | dBm  | 1     |
| Eye Mask Coordinates:           |                                  |         | tion Values |         |      |       |
| X1, X2, X3, Y1, Y2, Y3          | 0.25, 0.4, 0.45, 0.25, 0.28, 0.4 |         |             |         |      |       |
| Receiver                        |                                  |         |             |         |      |       |
| Sensitivity per Channel         | S                                |         |             | -8.6    | dBm  | 3     |
| Damage Threshold (each channel) | POL                              | 4.5     |             |         | dBm  |       |
| Optical Return Loss             | ORL                              | 26      |             |         | dB   |       |
| LOS De-Assert                   | LOSD                             |         |             | -11.6   | dBm  |       |
| LOS Assert                      | LOSA                             | -24     |             |         | dBm  |       |
| LOS Hysteresis                  |                                  | 0.5     |             |         | dB   |       |

## Notes:

- 1. The optical power is launched into SMF.
- 2. Measured with a PRBS 2<sup>31</sup>-1 test pattern @25.78125Gbps.
- 3. Measured with PRBS 2<sup>31</sup>-1 test pattern, 25.78125Gb/s

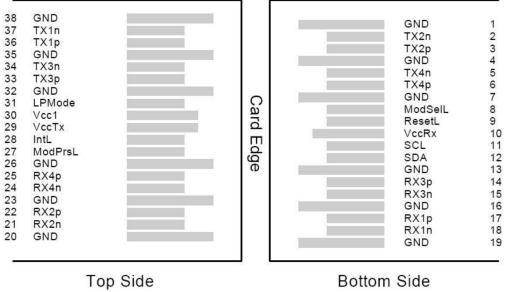


| Pin | Logic      | Symbol  | Name/Descriptions                                   | Ref. |
|-----|------------|---------|---|------|
| 1   |            | GND     | Module Ground                                       | 1    |
| 2   | CML-I      | Tx2-    | Transmitter inverted data input                     |      |
| 3   | CML-I      | Tx2+    | Transmitter non-inverted data input                 |      |
| 4   |            | GND     | Module Ground                                       | 1    |
| 5   | CML-I      | Tx4-    | Transmitter inverted data input                     |      |
| 6   | CML-I      | Tx4+    | Transmitter non-inverted data input                 |      |
| 7   |            | GND     | Module Ground                                       | 1    |
| 8   | LVTTL-I    | MODSEIL | Module Select                                       | 2    |
| 9   | LVTTL-I    | ResetL  | Module Reset  | 2    |
| 10  |            | VCCRx   | +3.3v Receiver Power Supply                         |      |
| 11  | LVCMOS-I   | SCL     | 2-wire Serial interface clock                       | 2    |
| 12  | LVCMOS-I/O | SDA     | 2-wire Serial interface data                        | 2    |
| 13  |            | GND     | Module Ground                                       | 1    |
| 14  | CML-O      | RX3+    | Receiver non-inverted data output                   |      |
| 15  | CML-O      | RX3-    | Receiver inverted data output                       |      |
| 16  |            | GND     | Module Ground                                       | 1    |
| 17  | CML-O      | RX1+    | Receiver non-inverted data output                   |      |
| 18  | CML-O      | RX1-    | Receiver inverted data output                       |      |
| 19  |            | GND     | Module Ground                                       | 1    |
| 20  |            | GND     | Module Ground                                       | 1    |
| 21  | CML-O      | RX2-    | Receiver inverted data output                       |      |
| 22  | CML-O      | RX2+    | Receiver non-inverted data output                   |      |
| 23  |            | GND     | Module Ground                                       | 1    |
| 24  | CML-O      | RX4-    | Receiver inverted data output                       |      |
| 25  | CML-O      | RX4+    | Receiver non-inverted data output                   |      |
| 26  |            | GND     | Module Ground                                       | 1    |
| 27  | LVTTL-O    | ModPrsL | Module Present, internal pulled down to GND         |      |
| 28  | LVTTL-O    | IntL    | Interrupt output, should be pulled up on host board | 2    |
| 29  |            | VCCTx   | +3.3v Transmitter Power Supply                      |      |
| 30  |            | VCC1    | +3.3v Power Supply                                  |      |
| 31  | LVTTL-I    | LPMode  | Low Power Mode                                      | 2    |
| 32  |            | GND     | Module Ground                                       | 1    |
| 33  | CML-I      | Tx3+    | Transmitter non-inverted data input                 |      |
| 34  | CML-I      | Tx3-    | Transmitter inverted data input                     |      |
| 35  |            | GND     | Module Ground                                       | 1    |
| 36  | CML-I      | Tx1+    | Transmitter non-inverted data input                 |      |
| 37  | CML-I      | Tx1-    | Transmitter inverted data input                     |      |
| 38  |            | GND     | Module Ground                                       | 1    |

## Notes:

- 1. Module circuit ground is isolated from module chassis ground with in the module.
- 2. Open collector; should be pulled up with 4.7k-10k ohms on host board to a voltage between 3.15V and 3.6V.

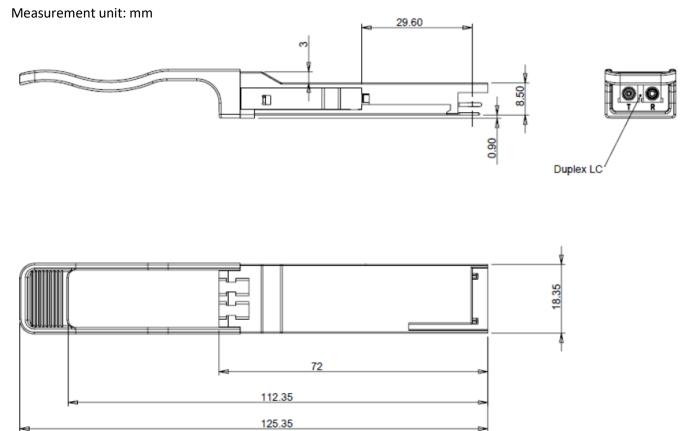
## **Electrical Pin-out Details**



Viewed from Top

Viewed from Bottom

# **Mechanical Specifications**



## **About ProLabs**

Our experience comes as standard; for over 15 years ProLabs has delivered optical connectivity solutions that give our customers freedom and choice through our ability to provide seamless interoperability. At the heart of our company is the ability to provide state-of-the-art optical transport and connectivity solutions that are compatible with over 90 optical switching and transport platforms.

## **Complete Portfolio of Network Solutions**

ProLabs is focused on innovations in optical transport and connectivity. The combination of our knowledge of optics and networking equipment enables ProLabs to be your single source for optical transport and connectivity solutions from 100Mb to 400G while providing innovative solutions that increase network efficiency. We provide the optical connectivity expertise that is compatible with and enhances your switching and transport equipment.

## **Trusted Partner**

Customer service is our number one value. ProLabs has invested in people, labs and manufacturing capacity to ensure that you get immediate answers to your questions and compatible product when needed. With Engineering and Manufacturing offices in the U.K. and U.S. augmented by field offices throughout the U.S., U.K. and Asia, ProLabs is able to be our customers best advocate 24 hours a day.



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