# Pro**Labs**

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## MMA1Z00-NS200-C

Mellanox<sup>®</sup> MMA1Z00-NS200 Compatible TAA 200GBase-SR2 QSFP112 Transceiver (MMF, 850nm, 50m, MPO, DOM, CMIS 5.0)

# Features:

- QSFP112 MSA Compliant
- 2x100G PAM4 retimed 400GAUI-4 electrical interface
- Compliant with IEEE 802.3db
- Compliant to IEEE 802.3ck
- Operating Temperature: 0 to 70 Celsius
- 4 channel VCSEL arrays and 4 channels PIN photo detector

arrays

- MPO-12 APC Connector
- CMIS 5.0
- Class 1 Laser
- Hot Pluggable QSFP112 Form Factor
- RoHS Compliant and Lead-Free

**Applications:** 

• 200GBase Ethernet

# **Product Description**

This Mellanox<sup>®</sup> MMA1Z00-NS200 compatible QSFP112 transceiver provides 200GBase-SR2 throughput up to 50m over multi-mode fiber (MMF) using a wavelength of 850nm via an MPO connector. It is guaranteed to be 100% compatible with the equivalent Mellanox<sup>®</sup> transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

ProLabs' transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



Rev. 092324

### **Absolute Maximum Ratings**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Power Supply Voltage	Vcc	-0.5		3.6	V	
Storage Temperature	Tstg	-40		85	°C	
Operating Case Temperature	Тс	0		70	°C	
Relative Humidity (non-condensing)	RH	15		85	%	
Receiver Damage Threshold, per Lane	PRdmg	5			dBm	
Bit Rate	BR			425	Gbps	
Fiber Length on OM3 MMF				60	m	
Fiber Length on OM4 MMF				100	m	
I2C Clock Frequency		0	10	1000	kHz	

#### Notes:

1. Exceeding the Absolute Maximum Ratings table may cause permanent damage to the device. This is just an emphasized rating and does not involve the functional operation of the device that exceeds the specifications of this technical specification under these or other conditions. Long-term operation under Absolute Maximum Ratings will affect the reliability of the device.

#### **Electrical Characteristics**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Power Supply Voltage	Vcc	3.135	3.3	3.465	V	
Total Power Consumption	Рс			9	W	1
Supply Current per end				2.72	A	
Pre FEC Bit Error Ratio				2.4E-4		
Post FEC Bit Error Ratio				1E-12		
Transmitter (each lane)						
Differential pk-pk Input Voltage Tolerance		750			mV	
Differential Termination Mismatch				10	%	
Eye Height	EH	10			mV	
Common-Mode to Differential-Mode Return Loss	RLDc	IEEE802.3ck	Equation (120	G—1)	dB	
Vertical Eye Closure	VEC			12	dB	
Effective Return Loss	ERL	7.3			dB	
Transition Time		10			ps	
Receiver (each lane)						
Differential Data Output Swing		300		900	mVpp	
Differential Termination Mismatch				10	%	

Eye Height	EH	15			mV	
Vertical Eye Closure	VEC			12	dB	
Common-Mode to Differential-Mode Return Loss	RLDc	IEEE802.3ck Equation (120G–1)			dB	
Effective Return Loss	ERL	8.5			dB	
Transition Time		8.5			ps	

Notes:

1. Under condition of 3.465V operating supply voltage, and  $70^{\circ}$ C case temperature.

# **Optical Characteristics**

Parameter		Symbol	Min.	Тур.	Max.	Unit	Notes
Transmitter							
Data Rate per la	ane	DR		53.125		GBd	
Modulation For	rmat			PAM4			
Center Waveler	ngth	λ	844	850	863	nm	1
RMS Spectral W	/idth	σ			0.6	nm	
Average Launch	verage Launch Power, each lane Pavg -4.6 4				4	dBm	
Optical Power OMA, each Lane, max		Рома	3.5			dBm	
OMAouter,	max (TECQ, TDECQ) <1.8 dB		max [-2.6	6 , max(TECQ,TE	CQ) – 4.4]	dBm	
each lane min	1.8 < max (TECQ, TDECQ) < 4.4 dB						
Transmitter and Dispersion Eye Closure (TDECQ), each lane		TDECQ			4.4	dB	
Transmitter Eye Closure for PAM4 (TECQ), each lane		TECQ			4.4	dB	
Extinction Ratio	)	ER	2.5			dB	
Transmitter Po	wer Excursion, each lane				2.3	dBm	
<b>Optical Return</b>	Loss Tolerance	ORLT			14	dB	
Optical Power	for TX DISABLE				-30	dBm	
Encircled Fluxb			≥86% at 19 um ≤30% at 4.5 um			2	
Receiver							
Data Rate per la	ane	BR		53.125		GBd	
Modulation For	rmat			PAM4	<u> </u>		
Center Wavelength		λ	844	850	863	nm	
Damage Threshold			5			dBm	
Average Receiv	ve Power, each Lane	AOP <sub>R</sub>	-6.4		4	dBm	
<b>Receive Power</b>	(OMAouter), each Lane	OMA <sub>R</sub>			3.5	dBm	

Receiver Reflectance	RR			-15	dB		
Receiver Sensitivity,	S	RS = max (-4.6 , TECQ – 6.4)			dBm	3	
Stressed Receiver Sensitivity, each Lane		SRS			-2.0	dBm	
RX LOS	Assert		-15			dBm	
	De-assert				-7.5	dBm	
	Hysteresis		0.5		5	dB	

## Notes:

- 1. Defined according to the performance of the laser used.
- 2. Measured into type A1a.2 or type A1a.3, or A1a.4, 50 um fiber, in accordance with IEC 61280-1-4.
- 3. Receiver sensitivity is informative and is defined for a transmitter with a value of TECQ. Measured with conformance test signal at TP3 for BER = 2.4E-4 Pre-FEC.

Byte	Bits	Field Name	Field Description
	6	Simultaneous Host and Media Side loopbacks	0b: not supported
	5	Per Lane Media Side Loopbacks	1b: supported
	4	Per Lane Host Side Loopbacks	1b: supported
13h:128	3	Host Side Input Loopback	1b: supported
	2	Host Side Output Loopback	1b: supported
	1	Media Side Input Loopback	1b: supported
	0	Media Side Output Loopback	1b: supported

# **QSFP-DD Rx Output Equalization Code Table**

# **Pin Descriptions**

Pin	Logic	Symbol	Name/Description	Power Sequence	Notes
1		Ground	GND	1B	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3B	
3	CML-I	Тх2р	Transmitter Non-Inverted Data Input	3B	
4		Ground	GND	1B	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3B	
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	3B	
7		Ground	GND	1B	1
8	LVTTL-I	ModSelL	Module Select	3B	
9	LVTTL-I	ResetL	Module Reset	3B	
10		VccRx	+3.3V Power Supply Receiver	2B	2
11	LVCMOS-I/O	SCL	2-wire serial interface clock	3B	

12	LVCMOS-I/O	SDA	2-wire serial interface data	3B	
13		Ground	GND	18	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3B	
15	CML-O	Rx3n	Receiver Inverted Data Output	3B	
16		Ground	GND	18	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3B	
18	CML-O	Rx1n	Receiver Inverted Data Output	3B	
19		Ground	GND	1B	1
20		Ground	GND	1B	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3B	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3B	
23		Ground	GND	1B	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3B	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3B	
26		Ground	GND	1B	1
27	LVTTL-O	ModPrsL	Module Present	3B	
28	LVTTL-0	IntL	Interrupt	3B	
29		VccTx	+3.3V Power supply transmitter	2B	2
30		Vcc1	+3.3V Power supply	2B	2
31	LVTTL-I	LPMode	Low Power mode	3B	
32		Ground	GND	1B	1
33	CML-I	Тх3р	Transmitter Non-Inverted Data Input	3B	
34	CML-I	Tx3n	Transmitter Inverted Data Input	3B	
35		Ground	GND	1B	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3B	
37	CML-I	Tx1n	Transmitter Inverted Data Input	3B	
38		Ground	GND	18	1

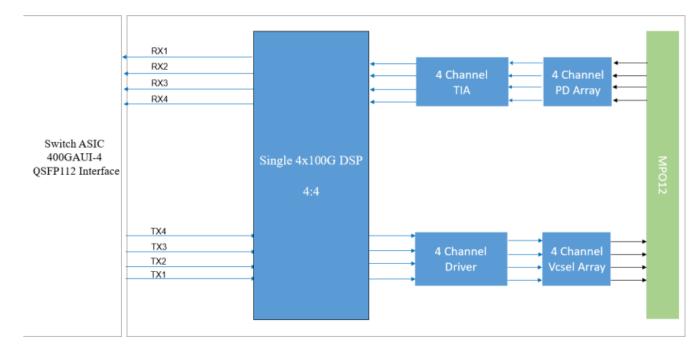
# Notes:

- 1. GND is the symbol for signal and supply (power) common for the QSFP112 module. All are common within the QSFP112 module and all voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.
- Vcc Rx, Vcc1 and VccTx are the receiver and transmitter power supplies and shall be applied concurrently. VccRx, Vcc1 and VccTx may be internally connected within the QSFP112 module in any combination. The connector pins are each rated for a maximum current of 1.5A (max. current of 2.0 A is required for high module power of 15-20W).

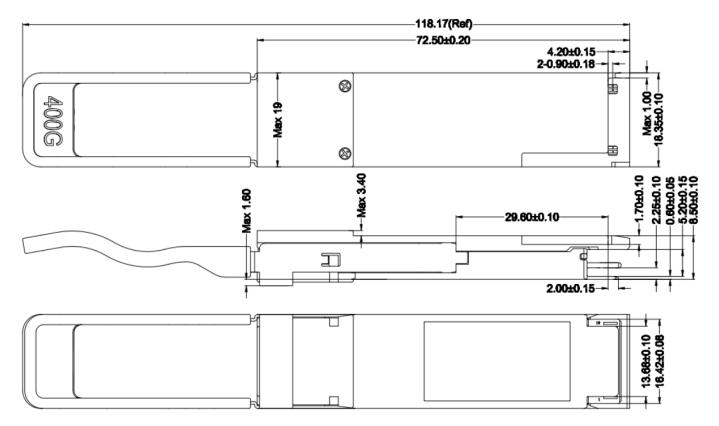
# **Electrical Pad Layout**

		7				
38	GND	/	/		CND	1
37	TX1n	/	/		GND	
36	TX1p	1	/		TX2n	2
35	GND				TX2p	3
34	TX3n				GND	4
33	ТХЗр				TX4n	5
32	GND				TX4p	6
31	LPMode / TxDis				GND	7
30	Vcc1		\	Ν	IodSeIL	8
29	VccTx				ResetL	9
28	Top view of board IntL / RxLOS			bottom view of board	VccRx	10
27	ModPrsI		$\langle \rangle$		SCL	11
					SDA	12
26	GND		1 1		GND	13
25	RX4p				RX3p	14
24	RX4n				RX3n	15
23	GND				GND	16
22	RX2p				RX1p	17
21	RX2n				RX1p	18
20	GND					
			/ /		GND	19
		/				

# **Functional Block Diagram**



# **Mechanical Specifications**



### **About ProLabs**

Our experience comes as standard; for over 15 years ProLabs has delivered optical connectivity solutions that give our customers freedom and choice through our ability to provide seamless interoperability. At the heart of our company is the ability to provide state-of-the-art optical transport and connectivity solutions that are compatible with over 90 optical switching and transport platforms.

#### **Complete Portfolio of Network Solutions**

ProLabs is focused on innovations in optical transport and connectivity. The combination of our knowledge of optics and networking equipment enables ProLabs to be your single source for optical transport and connectivity solutions from 100Mb to 400G while providing innovative solutions that increase network efficiency. We provide the optical connectivity expertise that is compatible with and enhances your switching and transport equipment.

# **Trusted Partner**

Customer service is our number one value. ProLabs has invested in people, labs and manufacturing capacity to ensure that you get immediate answers to your questions and compatible product when needed. With Engineering and Manufacturing offices in the U.K. and U.S. augmented by field offices throughout the U.S., U.K. and Asia, ProLabs is able to be our customers best advocate 24 hours a day.



Contact Information ProLabs US Email: sales@prolabs.com Telephone: 952-852-0252

ProLabs UK

Email: salessupport@prolabs.com Telephone: +44 1285 719 600