

QSFP28-2QSFP28-AOC3M-DE-C

Dell® Compatible TAA Compliant 100GBase-CU QSFP28 to 2xQSFP28 Active Optical Cable (850nm, MMF, 3m)

Features:

- Single 3.3V Power Supply
- Up to 25.78 Gbps per channel
- Low power consumption: 1.65W on 100G end
- 0.95W on 50G end with all CDRs enabled
- Operating temperature: 0 to 70 Celsius
- Hot Pluggable
- RoHS compliant and Lead Free



Applications:

- 50/100G Ethernet
- Data center: Switches, servers, storages and NIC adapters

Product Description

This is a Dell® Compatible 100GBase-AOC QSFP28 to 2xQSFP28 active optical cable that operates over active fiber with a maximum reach of 3m. It has been programmed, uniquely serialized, and data-traffic and application tested to ensure it is 100% compliant and functional. We stand behind the quality of our products and proudly offer a limited lifetime warranty. This cable is TAA (Trade Agreements Act) compliant and is built to comply with MSA (Multi-Source Agreement) standards.

ProLabs' transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Storage Temperature	Tstg	-40		85	°C	1
Operating Case Temperature	Tc	0		70		
Power Supply Voltage	Vcc	0		3.6	V	
Relative Humidity	RH	0		85	%	
Data Rate (Per Lane)	DR		25.78		Gbps	
Minimum Bend Radius		30			mm	2
		60			mm	3
Length Tolerance		30m: +500mm/-0mm				

Notes:

1. Ambient.
2. Without tension.
3. Under maximum tension.

Electrical Characteristics

Parameter		Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage		Vcc	3.13	3.3	3.47	V	
Power Supply Current	100G End	Icc		500		mA	1
	50G End			290			
Power Consumption	100G End			1.65	1.73	W	1
	50G End			0.95	1		
Transmitter							
Input Differential Impedance		RIN	90	100	110	Ω	
Differential Data Input Voltage		VIN,pp	200		900	mV	
Receiver							
Output Differential Impedance		ROUT	90	100	110	Ω	
Differential Data Output Voltage		VOUT,pp		800		mV	
Bit Error Ratio					10 ⁻⁸		2

Notes:

1. Per end.
2. Pre-FEC Bit Error Ratio with a PRBS 2³¹ – 1 test pattern.

