

SFP-10GBASE-LR-20-EX-C

Extreme Networks® Compatible TAA 10GBase-LR SFP+ Transceiver (SMF, 1310nm, 20km, LC, DOM)

Features:

- Compliant with IEEE802.3ae 10GBASE-LR/LW
- Compliant with MSA SFP+ Specification SFF-8431
- 1310nm DFB-LD Transmitter
- Distance up to 20km
- Single 3.3V Power Supply and TTL Logic Interface
- Duplex LC Connector
- Commercial Temperature 0 to 70 Celsius
- Hot-Pluggable
- Metal with Lower EMI
- Excellent ESD Protection
- RoHS compliant and Lead Free



Applications:

- 1000Base-LX/10GBASE-LR Ethernet
- 1x Fibre Channel
- Access and Enterprise

Product Description

This Extreme Networks® SFP+ transceiver provides 10GBase-LR throughput up to 20km over single-mode fiber (SMF) using a wavelength of 1310nm via an LC connector. It is guaranteed to be 100% compatible with the equivalent Extreme Networks® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

ProLabs' transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



Absolute Maximum Ratings

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Notes |
|----------------------------|--------|------|---------|-------------------|------|-------|
| Maximum Supply Voltage | Vcc | -0.5 | | 4 | V | 1 |
| Storage Temperature | Tstg | -40 | | 85 | °C | |
| Operating Case Temperature | Тс | 0 | | 70 | °C | |
| Relative Humidity | RH | 0 | | 85 | % | |
| Data Rate | DR | 9.83 | 10.3125 | 11.3 | Gb/s | 2 |
| Bit Error Rate | BER | | | 10 ⁻¹² | | |

Notes:

- 1. For electrical interface
- 2. IEEE 802.3ae

Electrical Characteristics

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Notes | |
|--------------------------------------|--------------------------------|------|------|----------------------|------|-------|--|
| Module Supply Voltage | Vcc | 3.14 | 3.3 | 3.46 | V | | |
| Module Supply Current | Icc | | 200 | 310 | mA | | |
| Power Dissipation | PD | | 0.65 | 1.0 | W | | |
| Transmitter | | | | | | | |
| Input Differential Impedance | RIN | | 100 | | Ω | | |
| Differential Data Input Swing | VIN PP | 180 | | 700 | mV | | |
| Transmit Disable Voltage | V _D | 2 | | VCC | V | | |
| Transmit Enable Voltage | VEN | VEE | | V _{EE} +0.8 | V | | |
| Receiver | | | | | | | |
| Differential Data Output Swing | VOUT PP | 300 | | 850 | mV | | |
| Data Output Rise/Fall Time (20%-80%) | t _r /t _f | 28 | | | ps | | |
| LOS Assert | VLOS A | 2 | | VCC HOST | V | | |
| LOS De-Assert | VLOS D | VEE | | V _{EE} +0.5 | V | | |

Optical Characteristics

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Notes |
|---------------------------------|------------------|-------|------|-------|-------|-------|
| Transmitter | | | | | | |
| Output Optical Power | РТХ | -8.2 | | 0.5 | dBm | 1 |
| Optical Center Wavelength | λ _C | 1260 | | 1355 | nm | |
| Optical Modulation Amplitude | OMA | -5.2 | | | dBm | 2 |
| Extinction Ratio | ER | 3.5 | 5.5 | | dB | |
| Spectral Width(-20dB) | Δλ | | | 1 | nm | |
| Side Mode Suppression Ratio | SMSR | 30 | | | dB | |
| Relative Intensity Noise | RIN | | | -128 | dB/Hz | |
| Transmitter Dispersion Penalty | TDP | | | 3.2 | dB | |
| Launch Power of OFF Transmitter | POUT_OFF | | | -30 | dBm | 1 |
| Transmitter Jitter | | | | | | 2 |
| Receiver | | | | | | |
| Optical Center Wavelength | λ _C | 1260 | | 1600 | nm | |
| Average Receive Power | PRX | -14.4 | | 0.5 | dBm | |
| Receiver Sensitivity @10.3Gb/s | RX_SEN | | | -14.4 | dBm | 3 |
| Receiver Reflectance | TR _{RX} | | | -12 | dB | |
| LOS Assert | LOS _A | -30 | | | dBm | |
| LOS De-Assert | LOS _D | | | -17 | dBm | |
| LOS Hysteresis | LOS _H | 0.5 | | | dB | |

Notes:

- 1. Average
- 2. According to IEEE 802.3ae requirement.
- 3. Test the resulting value using the minimum ER value within the defined range; BER<10 $^{-12}$; 2^{31} -1 PRBS.

Pin Descriptions

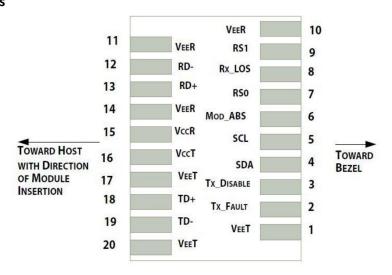
| Pin | Symbol | Name/Descriptions | Ref. |
|-----|------------|--|------|
| 1 | VeeT | Transmitter Ground. | 1 |
| 2 | Tx_Fault | Transmitter Fault. LVTTL-O. "High" indicates a fault condition. | 2 |
| 3 | Tx_Disable | Transmitter Disable. LVTTL-I. "High" or "open" disables the transmitter. | 3 |
| 4 | SDA | 2-Wire Serial Interface Data. LVCMOS-I/O. MOD-DEF2. | 4 |
| 5 | SCL | 2-Wire Serial Interface Clock. LVCMOS-I/O. MOD-DEF1. | 4 |
| 6 | MOD_ABS | Module Absent (Output). Connected to VeeT or VeeR in the module. | 5 |
| 7 | RS0 | N/A. | 6 |
| 8 | Rx_LOS | Receiver Loss of Signal. LVTTL-O. | 2 |
| 9 | RS1 | N/A. | 6 |
| 10 | VeeR | Receiver Ground. | 1 |
| 11 | VeeR | Receiver Ground. | 1 |
| 12 | RD- | Inverse Received Data Out. CML-O. | |
| 13 | RD+ | Received Data Out. CML-O. | |
| 14 | VeeR | Receiver Ground. | |
| 15 | VccR | +3.3V Receiver Power. | |
| 16 | VccT | +3.3V Transmitter Power. | |
| 17 | VeeT | Transmitter Ground. | 1 |
| 18 | TD+ | Transmitter Data In. CML-I. | |
| 19 | TD- | Inverse Transmitter Data In. CML-I. | |
| 20 | VeeT | Transmitter Ground. | 1 |

Notes:

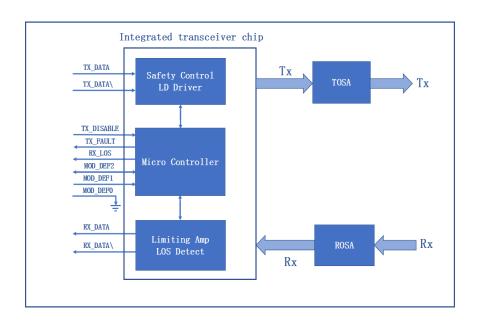
- 1. The module signal grounds are isolated from the module case.
- 2. This is an open collector/drain output that on the host board requires a $4.7K\Omega$ to $10K\Omega$ pull-up resistor to Host_Vcc.
- 3. This input is internally biased high with a $4.7K\Omega$ to $10K\Omega$ pull-up resistor to VccT.
- 4. 2-Wire Serial Interface Clock and Data lines require an external pull-up resistor dependent on the capacitance load.
- 5. This is a ground return that, on the host board, requires a 4.7K Ω to 10K Ω pull-up resistor to the Host_Vcc.
- 6. Rate select can also be set through the 2-wire bus in accordance with SFF-8472 v. 12.1. Rx Rate Select is set at Bit 3, Byte 110, and Address A2h, and Tx Rate Select is set at Bit 3, Byte 118, and Address A2h.

 Note: Writing a "1" selects maximum bandwidth operation. Rate select is the logic OR of the input state of Rate Select Pin and 2-wire bus.

Electrical Pin-out Details



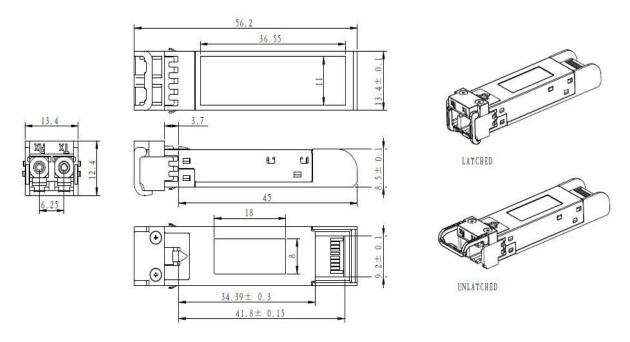
Block Diagram



Mechanical Specifications

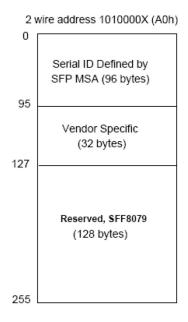
ALL DIMENSIONS ARE ±0.2mm UNLESS OTHERWISE SPECIFIED

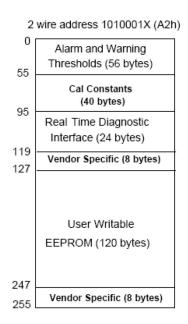
UNIT: mm



EEPROM Information

EEPROM memory map-specific data field description is as below:





About ProLabs

Our experience comes as standard; for over 15 years ProLabs has delivered optical connectivity solutions that give our customers freedom and choice through our ability to provide seamless interoperability. At the heart of our company is the ability to provide state-of-the-art optical transport and connectivity solutions that are compatible with over 90 optical switching and transport platforms.

Complete Portfolio of Network Solutions

ProLabs is focused on innovations in optical transport and connectivity. The combination of our knowledge of optics and networking equipment enables ProLabs to be your single source for optical transport and connectivity solutions from 100Mb to 400G while providing innovative solutions that increase network efficiency. We provide the optical connectivity expertise that is compatible with and enhances your switching and transport equipment.

Trusted Partner

Customer service is our number one value. ProLabs has invested in people, labs and manufacturing capacity to ensure that you get immediate answers to your questions and compatible product when needed. With Engineering and Manufacturing offices in the U.K. and U.S. augmented by field offices throughout the U.S., U.K. and Asia, ProLabs is able to be our customers best advocate 24 hours a day.















Contact Information

ProLabs US

Email: sales@prolabs.com Telephone: 952-852-0252

ProLabs UK

Email: salessupport@prolabs.com Telephone: +44 1285 719 600